# ASIC Implementation of Digital Backpropagation with Deep-Learned Chromatic Dispersion Filters

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Introduction 000	Deep Learning 000000	Joint Filter Optimization	ASIC Implementation	Results 000	Conclusions O	CHALMERS
			Outline			

- 1. Introduction to Digital Backpropagation
- 2. Connection between Deep Learning and Digital Backpropagation
- 3. Joint Chromatic Dispersion Filter Optimization
- 4. ASIC Implementation Aspects
- 5. Results: Performance, Power Consumption, and Chip Area
- 6. Conclusions

Introduction •00	Deep Learning 000000	ASIC Implementation		CHALMERS
		Outline		

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- 2. Connection between Deep Learning and Digital Backpropagation
- 3. Joint Chromatic Dispersion Filter Optimization
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 Invert a partial differential equation in real time ([Paré et al., 1996], [Essiambre and Winzer, 2005], [Roberts et al., 2006], [Li et al., 2008], [Ip and Kahn, 2008])



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• Split-step Fourier method with M steps ( $\delta = L/M$ ):



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Introduction 000	Deep Learning 000000			CHALMERS

• Widely considered to be impractical (too complex)

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000				CHALMERS

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Introduction	Deep Learning	Joint Filter Optimization	ASIC Implementation	Results	Conclusions O	CHAIMEDO
				000		CHALMERS

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- Comparable to published results for static chromatic dispersion (CD) compensation
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#### Key ingredients

- 1. No FFT/IFFT: We use finite-impulse response (FIR) filters to compensate for CD-induced pulse broadening in each step.
- 2. Deep learning: The FIR filters are jointly optimized and quantized using machine-learning tools.
- 3. No step-reducing approaches: 64-step DBP (2 steps per span) would consume only marginally more power, not  $2 \times$  more.

Introduction 000	Deep Learning ●00000	Joint Filter Optimization 000	ASIC Implementation	Results 000	Conclusions O	CHALMERS
			Outline			

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# Complexity-Reduced Digital Backpropagation







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- Therefore, reduce *M* as much as possible (step-reducing approaches)
- Intuitive, but ...
- ... this corresponds to flattening a deep (multi-layer) computation graph
- Machine learning: deep computation graphs work much better and are more parameter efficient than shallow ones





How to choose  $f_{\theta}(\boldsymbol{y})$ ? Deep feed-forward neural networks





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How to optimize  $\theta = \{ W^{(1)}, \dots, W^{(\ell)}, b^{(1)}, \dots, b^{(\ell)} \}$ ? Deep learning

$$\min_{\theta} \sum_{i=1}^{N} \mathsf{Loss}(f_{\theta}(\boldsymbol{y}^{(i)}), \boldsymbol{x}^{(i)}) \triangleq g(\theta) \quad \text{using} \quad \theta_{k+1} = \theta_k - \lambda \nabla_{\theta} g(\theta_k) \quad (1)$$



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#### Time-Domain Implementation and Truncation



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#### Time-Domain Implementation and Truncation



 $n \gg 9$ 

$h_0 h_1 h_2 h_3 h_4 h_5 h_6 h_7 h_8 h_9$
$h_1 \ h_0 \ h_1 \ h_2 \ h_3 \ h_4 \ h_5 \ h_6 \ h_7 \ h_8$
$h_2 h_1 h_0 h_1 h_2 h_3 \overline{h_4} h_5 h_6 h_7 \cdots$
$h_3 h_2 h_1 h_0 h_1 h_2 h_3 h_4 h_5 h_6 \approx 0$
$h_4 \ h_3 \ h_2 \ h_1 \ h_0 \ h_1 \ h_2 \ h_3 \ \overline{h_4} \ h_5$
$h_5 h_4 h_3 h_2 h_1 h_0 h_1 h_2 h_3 h_4$
$h_6 \ h_5 \ h_4 \ h_3 \ h_2 \ h_1 \ h_0 \ h_1 \ h_2 \ h_3$
$h_7 \ h_6 \ h_5 \ h_4 \ h_3 \ h_2 \ h_1 \ h_0 \ h_1 \ h_2$
$h_8 \ h_7 \ h_6 \ h_5 \ h_4 \ h_3 \ h_2 \ h_1 \ h_0 \ h_1$
$h_9 \ h_8 \ h_7 \ h_6 \ h_5 \ h_4 \ h_3 \ h_2 \ h_1 \ h_0$
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#### Time-Domain Implementation and Truncation







finite impulse response (FIR) filter



 $\begin{array}{l} \text{symmetric filter coefficients} \\ \Longrightarrow & \text{folded implementation} \end{array}$ 



#### Time-Domain Digital Backpropagation: Literature













#### Nontrivial to achieve a good performance-complexity tradeoff!

Example for  $R_{symb} = 10.7$  Gbaud, L = 2000 km [lp and Kahn, 2008]



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Example for  $R_{symb} = 10.7$  Gbaud, L = 2000 km [lp and Kahn, 2008]

•  $\gg 1000$  taps required for good performance (70 taps per step)



#### Problem: Truncation Errors



$$h^{(1)} = h^{(2)} = \cdots = h^{(M)}$$



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$$\boldsymbol{h}^{(1)} * \boldsymbol{h}^{(2)} * \cdots * \boldsymbol{h}^{(M)}$$

Our approach: Optimize all M filters jointly

Introduction 000	Deep Learning 000000	Joint Filter Optimization ●00	ASIC Implementation	Results 000	Conclusions O	CHALMERS
			Outline			

- 1. Introduction to Digital Backpropagation
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- 6. Conclusions

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Joint Chromatic Dispersion Filter Optimization via Deep Learning



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**TensorFlow implementation** of the computation graph  $f_{\theta}(\boldsymbol{y})$ :





Joint Chromatic Dispersion Filter Optimization via Deep Learning

**TensorFlow implementation** of the computation graph  $f_{\theta}(\boldsymbol{y})$ :



Deep learning of parameters  $\theta = \{ \boldsymbol{h}^{(1)}, \dots, \boldsymbol{h}^{(M)} \}$ :

$$\min_{\theta} \sum_{i=1}^{N} \mathsf{Loss}(f_{\theta}(\boldsymbol{y}^{(i)}), \boldsymbol{x}^{(i)}) \triangleq g(\theta)$$
  
mean squared error

using  $\theta_{k+1} = \theta_k - \lambda \nabla_{\theta} g(\theta_k)$ Adam optimizer, fixed learning rate

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# Iterative Filter Tap Pruning

$$heta = \left\{egin{array}{cc} oldsymbol{h}^{(1)} & & \ oldsymbol{h}^{(2)} & & \ dots & & \ dots & & \ oldsymbol{h}^{(M)} & & \ oldsymbol{h}^{(M$$

• Initially: constrained least-squares coefficients (LS-CO) [Sheikh et al., 2016]



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- Initially: constrained least-squares coefficients (LS-CO) [Sheikh et al., 2016]
- Typical learning curve:



Introduction 000	Deep Learning 000000	Joint Filter Optimization	ASIC Implementation	Results 000	Conclusions O	CHALMERS
			Outline			

- 1. Introduction to Digital Backpropagation
- 2. Connection between Deep Learning and Digital Backpropagation
- 3. Joint Chromatic Dispersion Filter Optimization
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- 5. Results: Performance, Power Consumption, and Chip Area
- 6. Conclusions

Deep Learning	ASIC Implementation		
	000		CHALMERS

## Filter Coefficient Quantization



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DSP implementation requires quantized FIR filter coefficients.



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- Fake quantization: gradient computation and parameter updates are still performed in floating point
- Activate after the (floating-point) optimization has converged and continue training for few more iterations
- Joint optimization of quantized impulse responses ⇒ partial cancellation of quantization-induced frequency-response errors

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	000		CHALMERS

## Hardware Model and Circuit Implementation



#### Hardware Model and Circuit Implementation





• Signal requantization to s bits after each FIR filter and nonlinear step



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- Nonlinear steps via first-order Taylor expansion [Fougstedt et al., 2017a]:

$$xe^{\jmath\gamma\delta_{\ell}|x|^2} \approx x(1+\jmath\gamma\delta_{\ell}|x|^2)$$



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- 96-parallel VHDL implementation at 416.7 MHz clock speed (40 GHz RX signal), synthesized using a low-power 28-nm CMOS library
- All FIR filters are fully reconfigurable
- · Power estimation based on simulation of internal circuit switching statistics

Introduction 000	Deep Learning 000000	Joint Filter Optimization	ASIC Implementation	Results 000	Conclusions O	CHALMERS
			Outline			

- 1. Introduction to Digital Backpropagation
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#### Performance Results



System parameters:

- $32 \times 100 \text{ km}$  fiber
- 16-QAM single pol.
- RRC pulses (0.1 roll-off)
- 20 Gbaud
- 2 samples/symbol
- single channel



• Deep learning gives 15-tap filters with better performance



- Deep learning gives 15-tap filters with better performance
- 8-9 signal bits required in both cases, depending on performance
- Deep learning leads to significantly fewer bits for the filter taps

# Power (P) and Chip Area (A) Results Per Step

coeffs. &	filter	8-bi	t signal	9-bit signal		
word length	taps	P(W)	$A (mm^2)$	$\mid P(W)$	$A (mm^2)$	
LS-CO 8-bit LS-CO 9-bit	$25 \\ 25$	$\begin{array}{c} 0.28\\ 0.34\end{array}$	$\begin{array}{c} 1.21 \\ 1.38 \end{array}$	$\begin{array}{c c} 0.31 \\ 0.37 \end{array}$	$\begin{array}{c} 1.30 \\ 1.54 \end{array}$	
learned 5-bit learned 6-bit	15 15	$\begin{array}{c} 0.15 \\ 0.17 \end{array}$	$0.61 \\ 0.69$	0.18 0.20	$0.69 \\ 0.81$	

Introduction 000	Deep Learning 000000	Joint Filter ( 000	Optimization	ASIC Ir 000	nplementation	Results 00●	Conclusions O	CHALMERS
	Powe	er $(P)$ a	and Chi	ip Ar	ea $(A)$ l	Results	Per Ste	p
	C	oeffs. &	filter	8-bit	t signal $4  (mm^2)$	9-bit	signal $\frac{1}{4}$ (mm <sup>2</sup> )	
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• >40% power & area reduction for learned filters due to fewer taps and bits

Introduction 000	Deep Learning 000000	Joint Filter ( 000	Optimization	ASIC In 000	nplementation	Results 00●	Conclusions O	CHALMERS
	Powe	r(P) a	and Ch	ip Ar	ea $(A)$ l	Results	Per Ste	р
	co	effs. & d length	filter taps	8-bit P (W)	signal $A \ (mm^2)$	9-bit P (W)	signal $A \ (mm^2)$	
	LS-0 LS-0	CO 8-bit CO 9-bit	25 25	$\begin{array}{c} 0.28 \\ 0.34 \end{array}$	$\begin{array}{c} 1.21 \\ 1.38 \end{array}$	0.31 0.37	$1.30 \\ 1.54$	
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- > 40% power & area reduction for learned filters due to fewer taps and bits
- Estimate for 9-bit signal, 6-bit learned coefficients:
  - $33 \times 0.2 \text{ W} = 6.6 \text{ W} \text{ or } \approx 83 \text{ pJ/bit}$   $33 \times 0.81 \text{ mm}^2 = 27 \text{ mm}^2$

Introduction 000	Deep Learning 000000	Joint Filter Optimization	ASIC Implementation	Results 00●	Conclusions O	CHALMERS
	Powe	er $(P)$ and Ch	ip Area $(A)$	Results	Per Step	)
	co	effs. & filter	8-bit signal	9-bit s	signal	

coeffs. & filter word length taps		8-bit signal $P$ (W) $A$ (mm <sup>2</sup> )		$ \begin{vmatrix} 9 \\ 9 \\ P (W) & A (mm^2) \end{vmatrix} $	
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  - [Crivelli et al., 2014]: entire RX chip is 75 mm<sup>2</sup> with CD compensation occupying a relatively large fraction

Deep Learning 000000		Conclusions	CHALMERS

## Conclusions

Introduction 000	Deep Learning 000000	Joint Filter Optimization	r Optimization ASIC Implementation 000		Conclusions ●	CHALMERS

- Split-step digital backpropagation appears feasible for real-time DSP implementation using a time-domain approach for the linear steps
- Deep learning can be used to
  - jointly optimize all chromatic dispersion filters
  - prune filter taps to get very short filters
  - jointly quantize all filter coefficients
|             | Introduction<br>000 | Deep Learning<br>000000 | Joint Filter Optimization | ASIC Implementation | Results<br>000 | Conclusions<br>• | CHALMERS |
|-------------|---------------------|-------------------------|---------------------------|---------------------|----------------|------------------|----------|
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# Thank you! FIER-OPTIC COMMUNICATIONS RESEARCH CENTER

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