

Wideband Time-Domain Digital Backpropagation via Subband Processing and Deep Learning

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ECOC, Rome, Italy, September 25, 2018



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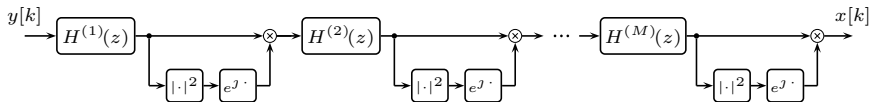
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Outline

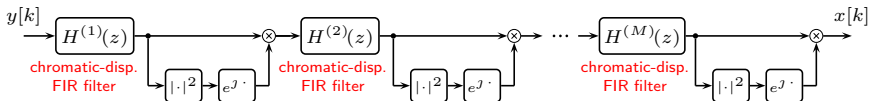
1. Introduction
2. Subband Processing via Filter Banks
3. Proposed DSP Architecture and Deep Learning
4. Results
5. Conclusions

Motivation: Real-Time Digital Backpropagation (DBP)

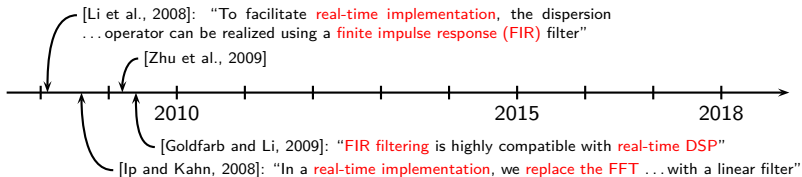


- **Real-time DBP** based on the **split-step Fourier method** is widely considered to be **impractical** due to many FFT/IFFT pairs

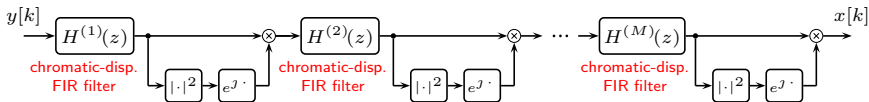
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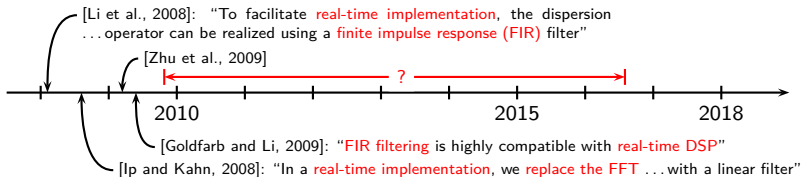
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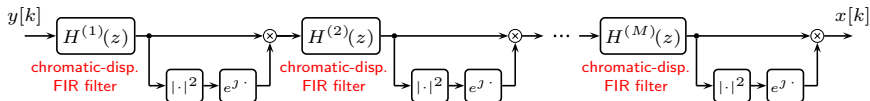
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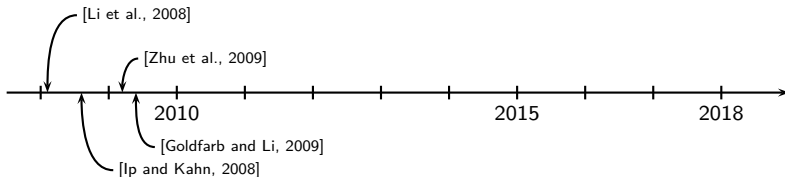
Complexity estimate [Ip and Kahn, 2008]

> 100× more operations than EDC

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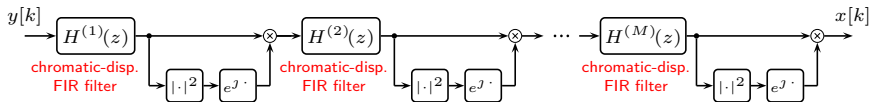
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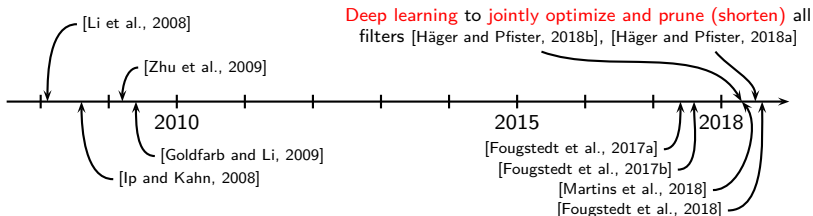
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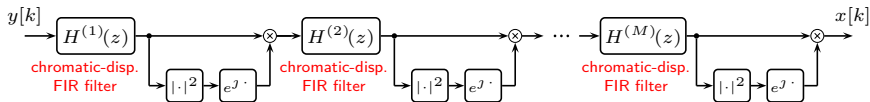
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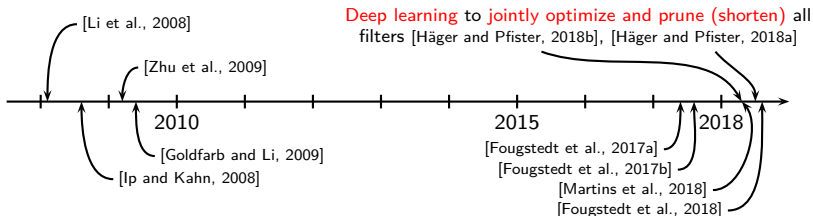
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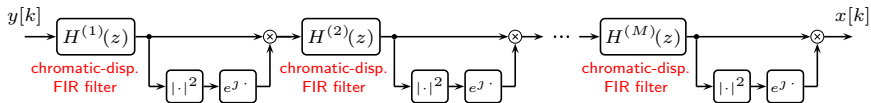
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Our ASIC design* (20 Gbaud, 28 nm):

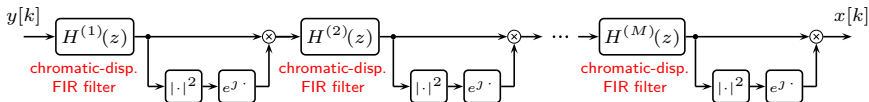
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*C. Fougstedt et al., tomorrow 13:30 in "DSP Techniques for Cost Effective Transmission"

This Talk: Wideband Scenarios

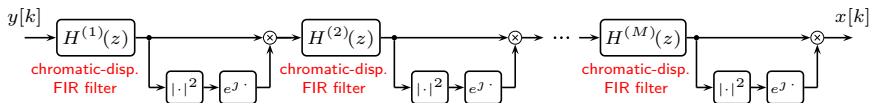


This Talk: Wideband Scenarios



- Complexity depends on **overall system memory** (total number of **filter taps**)
- Problem: **quadratic dependence** of **system memory** on the **bandwidth**

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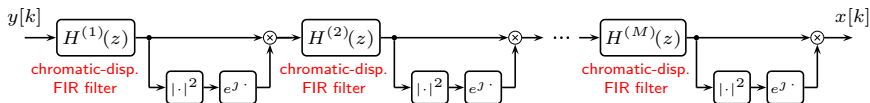
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Example

- ASIC power estimate* for 2500 km and 20 Gbaud (PM-16-QAM): **9 W**
- $5\times$ increase to **96-Gbaud** (or **superchannel** or **multiple WDM channels**)
- **Quadratic scaling**: $\approx 25 \times 9 \text{ W} = 225 \text{ W}$ (full DBP)
- **Linear scaling**: $\approx 5 \times 9 \text{ W} = 45 \text{ W}$ (5 independent receivers)

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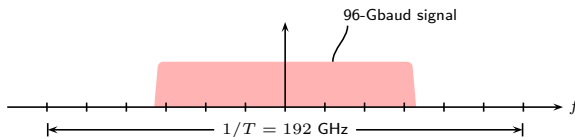
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Is it possible to **scale time-domain DBP** gracefully to **larger bandwidths**?

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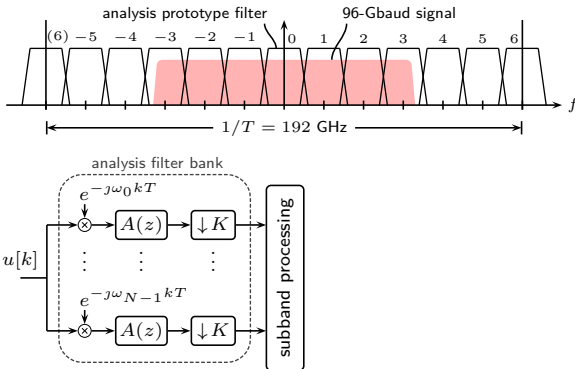
Subband Processing via Filter Banks

See, e.g., [Taylor, 2008], [Ho, 2009], [Slim et al., 2013], [Nazarathy and Tolmachev, 2014] (EDC) and [Mateo et al., 2010], [Ip et al., 2011], [Oyama et al., 2015] (nonlinearity compensation)



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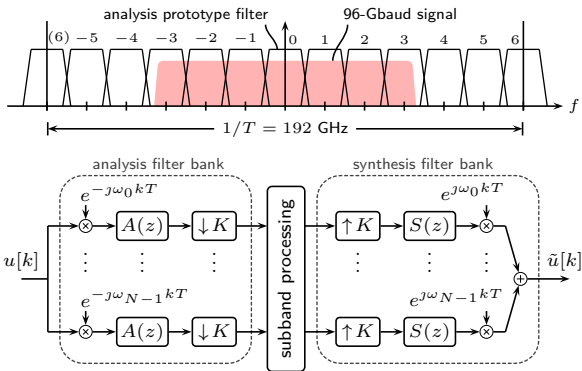
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- Split received signal into N parallel signals, then downsample by K

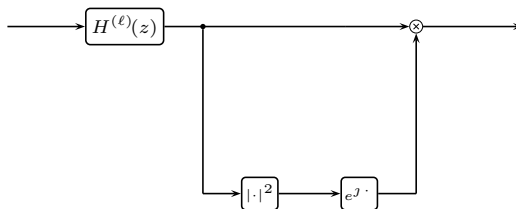
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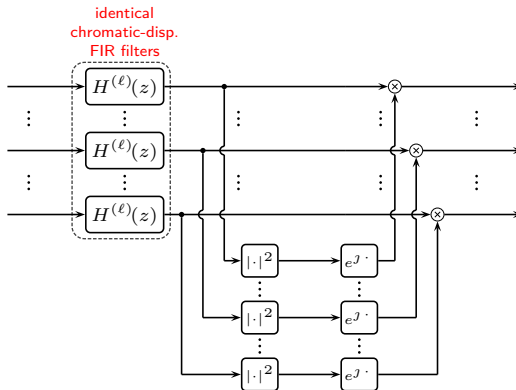


- Split received signal into N parallel signals, then downsample by K
- Synthesis filter bank reassembles the signal after processing

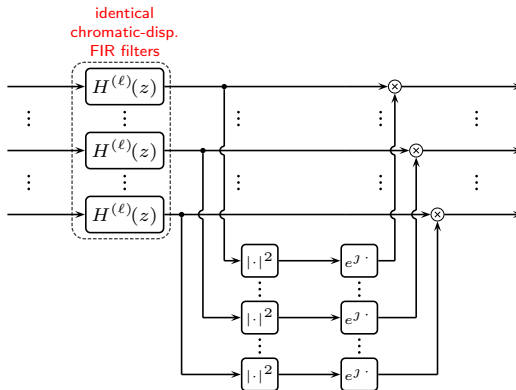
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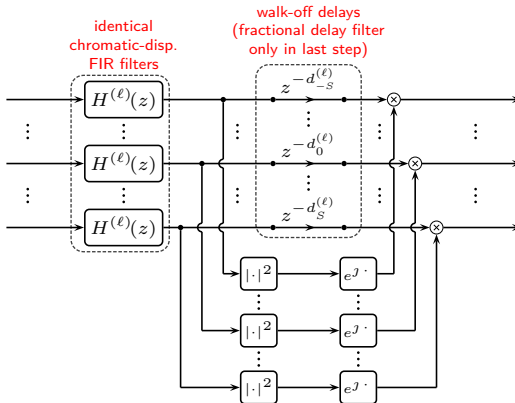


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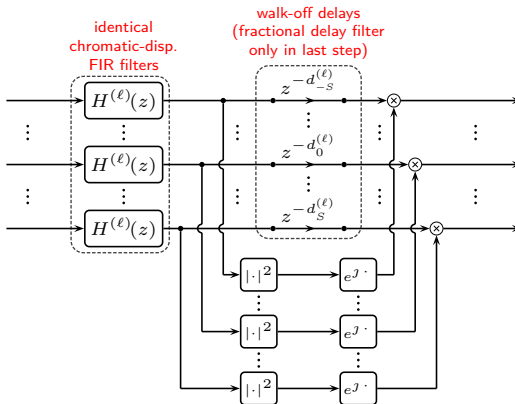
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 \Rightarrow choose step size such that delays are integer multiples of sampling interval

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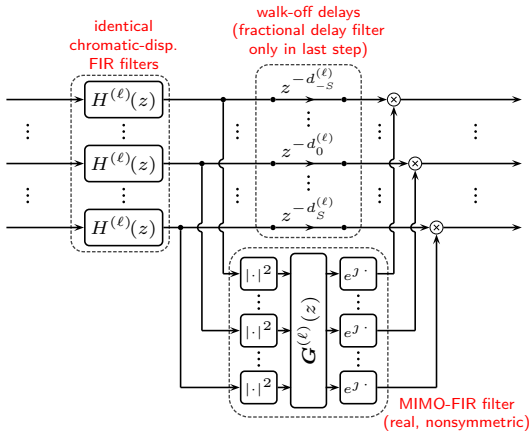


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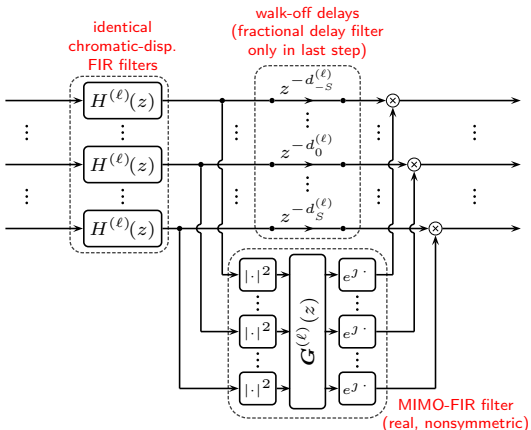
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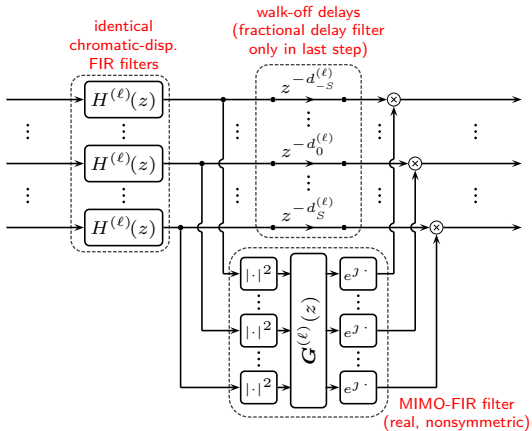


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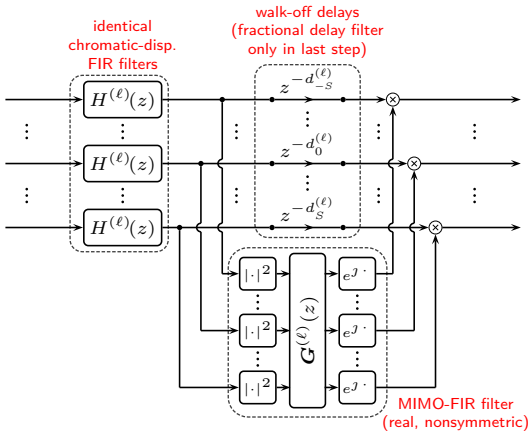


- **Hardware-efficient** implementation (**no FFT/IFFT**) of split-step method for **coupled NLSEs** [Leibrich and Rosenkranz, 2003], see also [Mateo et al., 2010]
- Only **accounts for XPM** between subbands, but not FWM

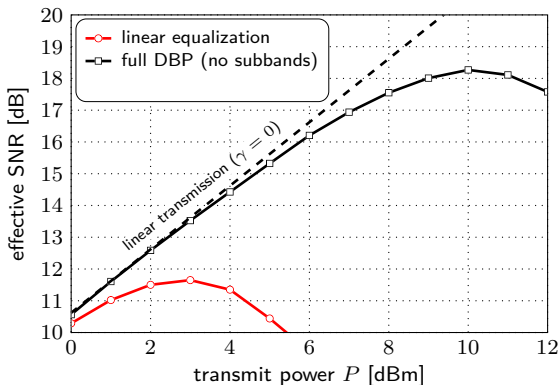
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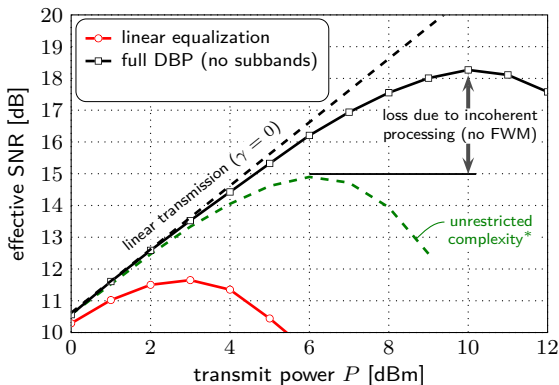
- “Unrolling” all steps gives a **deep, multi-layer computation graph**
- **Deep learning** to jointly optimize filters $H^{(\ell)}(z)$, $G^{(\ell)}(z)$ in all steps by maximizing **effective SNR** based on stochastic **gradient descent**
- Iteratively **prune** (set to 0) the outermost taps to get **very short filters**

Results ($N = 12$ Subbands for 96 Gbaud)

System parameters:

- 25×100 km fiber
- 96 Gbaud
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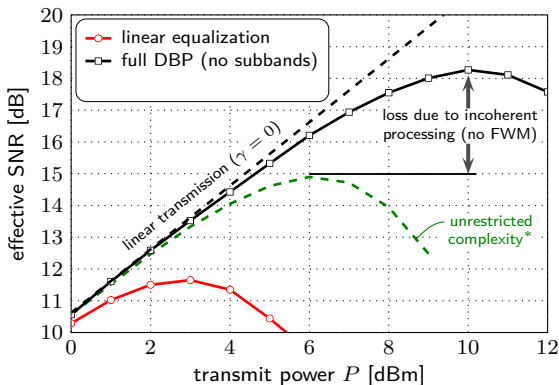


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 1000 steps/span

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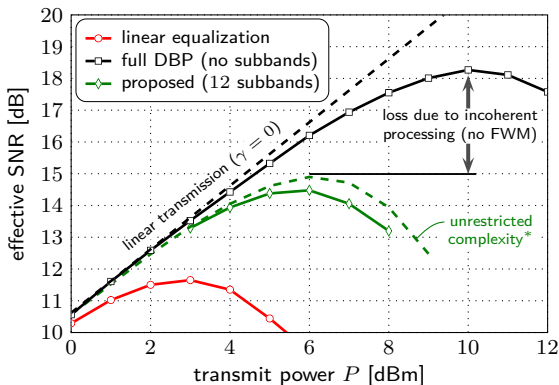
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- $K = 8$ ($1.5\times$ subband **oversampling**), 38.2 km step size (2.6 steps/span)

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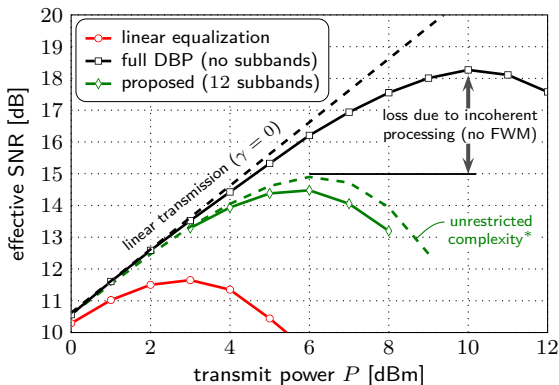
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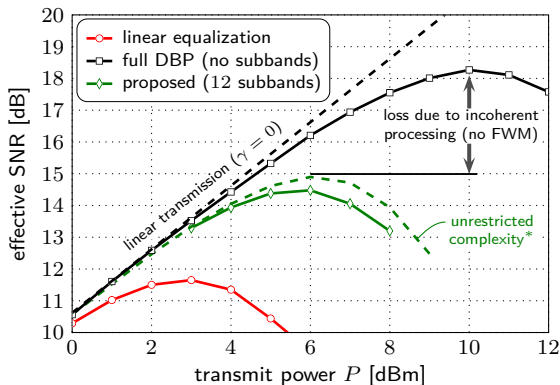
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- **7-tap learned filters** (16 real mul.), **sparse MIMO filters** (8 real mul.)

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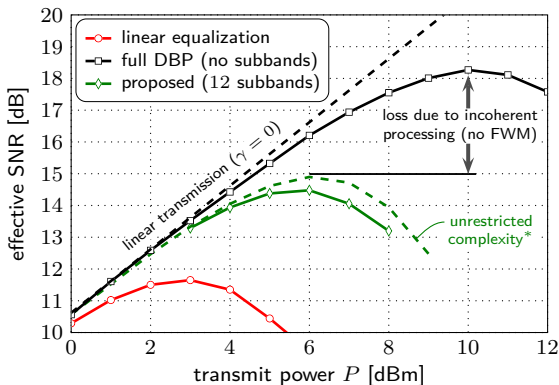


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- $> 4\times$ **less real mul.** compared to FFT/IFFT [Mateo et al., 2010]

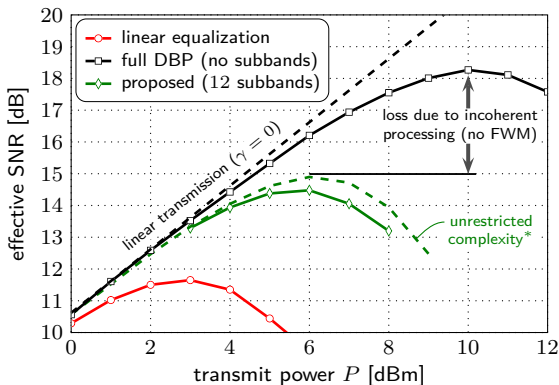
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- $\approx 2 - 3\times$ less complexity compared to full DBP (estimated):
 - Full DBP: ≈ 225 W
 - Subband processing: ≈ 75 W – 113 W
 - Independent receivers: ≈ 45 W

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Thank you!



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