Comparison of Terminated and Tailbiting Spatially Coupled LDPC Codes With Optimized Bit Mapping for PM-64-QAM

$\begin{array}{lll} \mbox{Christian Häger}^1 & \mbox{Alexandre Graell i Amat}^1 & \mbox{Fredrik Brännström}^1 \\ & \mbox{Alexandre}^2 & \mbox{Erik Agrell}^1 \end{array}$

¹Department of Signals and Systems, Chalmers University of Technology, Gothenburg, Sweden,

²Department of Electronic & Electrical Engineering, University College London, UK

{christian.haeger, alexandre.graell, fredrik.brannstrom, agrell}@chalmers.se, alex.alvarado@ieee.org

European Conference on Optical Communications (ECOC) Cannes, September 25, 2014





		CHALMERS

	SC-LDPC Codes		Results		
0	000	9	000	Ŭ	CHALMERS
		Mativatio			

• Large interest in designing spectrally efficient fiber-optical communication systems

System Model	SC-LDPC Codes	Bit Mapper Optimization	Results	Conclusions	CHALMERS
O	000	O	000	O	

- Large interest in designing spectrally efficient fiber-optical communication systems
- Spatially coupled low-density parity-check (SC-LDPC) codes are powerful candidates for forward error correction (FEC)

		CHALMERS

- Large interest in designing spectrally efficient fiber-optical communication systems
- Spatially coupled low-density parity-check (SC-LDPC) codes are powerful candidates for forward error correction (FEC)
- Termination responsible for excellent performance, but results in rate loss (i.e., higher FEC overhead, similar to conventional convolutional codes)

	Bit Mapper Optimization		
			CHALMERS

- Large interest in designing spectrally efficient fiber-optical communication systems
- Spatially coupled low-density parity-check (SC-LDPC) codes are powerful candidates for forward error correction (FEC)
- Termination responsible for excellent performance, but results in rate loss (i.e., higher FEC overhead, similar to conventional convolutional codes)
- What about tailbiting SC-LDPC codes? No termination, hence no rate loss, but also "bad" performance (comparable to the underlying uncoupled regular LDPC block code)

	Bit Mapper Optimization		
			CHALMERS

- Large interest in designing spectrally efficient fiber-optical communication systems
- Spatially coupled low-density parity-check (SC-LDPC) codes are powerful candidates for forward error correction (FEC)
- Termination responsible for excellent performance, but results in rate loss (i.e., higher FEC overhead, similar to conventional convolutional codes)
- What about tailbiting SC-LDPC codes? No termination, hence no rate loss, but also "bad" performance (comparable to the underlying uncoupled regular LDPC block code)
- Idea: use higher-order modulation format (here PM-64-QAM) with a tailbiting SC-LDPC code and optimized allocation of coded bits from FEC to modulation bits

	Bit Mapper Optimization		
			CHALMERS

- Large interest in designing spectrally efficient fiber-optical communication systems
- Spatially coupled low-density parity-check (SC-LDPC) codes are powerful candidates for forward error correction (FEC)
- Termination responsible for excellent performance, but results in rate loss (i.e., higher FEC overhead, similar to conventional convolutional codes)
- What about tailbiting SC-LDPC codes? No termination, hence no rate loss, but also "bad" performance (comparable to the underlying uncoupled regular LDPC block code)
- Idea: use higher-order modulation format (here PM-64-QAM) with a tailbiting SC-LDPC code and optimized allocation of coded bits from FEC to modulation bits

Main Result

Unequal error protection of a nonbinary modulation format can be used to significantly improve performance of tailbiting SC-LDPC codes. Comparable gap to capacity at a lower FEC overhead.

			CHALMERS
	Outling		Chintelineirte

Outline

- 1. System Model
- 2. SC-LDPC Codes
- 3. Bit Mapper Optimization
- 4. Results
- 5. Conclusions









• Coded bits from the FEC encoder



- Coded bits from the FEC encoder
- Modulator Φ : Gray-labeled PM-64-QAM





- Coded bits from the FEC encoder
- Modulator Φ : Gray-labeled PM-64-QAM
- Optical link: N_{sp} spans of standard single-mode fiber (SSMF) with lumped erbium-doped fiber amplifiers (EDFAs), no inline dispersion compensation





- Coded bits from the FEC encoder
- Modulator Φ : Gray-labeled PM-64-QAM
- Optical link: N_{sp} spans of standard single-mode fiber (SSMF) with lumped erbium-doped fiber amplifiers (EDFAs), no inline dispersion compensation
- Matched filter, sampling, equalization (electronic dispersion compensation)





- Coded bits from the FEC encoder
- Modulator Φ : Gray-labeled PM-64-QAM
- Optical link: N_{sp} spans of standard single-mode fiber (SSMF) with lumped erbium-doped fiber amplifiers (EDFAs), no inline dispersion compensation
- Matched filter, sampling, equalization (electronic dispersion compensation)
- Demodulator Φ^{-1} : log-likelihood ratio (LLR) computation

SC-LDPC Codes ●00		CHALMERS

Spatially Coupled LDPC Codes

• Can be represented in terms of a protograph = prototype graph

System Model	SC-LDPC Codes	Bit Mapper Optimization	Results	Conclusions	CHALMERS
O	O●O	O	000	O	
Exai	mple:	Terminated		Tailbit	ing











check node degrees

slightly irregular

regular



capacity-approaching (wave effect) comparable to regular LDPC (no wave effect)



SC-LDPC Codes 00●		CHALMERS

predicted BER per spatial position











SC-LDPC Codes 00●			CHALMERS
	Deceding M	lava	



System Model	SC-LDPC Codes	Bit Mapper Optimization	Results	Conclusions	CHALMERS
O	00●	O	000	O	



	SC-LDPC Codes 00●				CHALMERS



	SC-LDPC Codes 00●				CHALMERS







SC-LDPC Codes 00●		CHALMERS
	,	

predicted BER per spatial position



Successful decoding!





• Bit mapper determines allocation of n coded bits to m modulation bits (N = n/m)



• Bit mapper determines allocation of n coded bits to m modulation bits (N = n/m)

• Each protograph variable node (VN) corresponds to M coded bits in c


- Bit mapper determines allocation of n coded bits to m modulation bits (N = n/m)
- Each protograph variable node (VN) corresponds to M coded bits in c
- Fractional assignment of protograph VNs to modulation bits via matrix $\mathbf{A} = [a_{i,j}] \in \mathbb{R}^{m \times L}$, where $a_{i,j} =$ fraction of coded bits corresponding to VN j to be allocated to modulation bit i



- Bit mapper determines allocation of n coded bits to m modulation bits (N = n/m)
- Each protograph variable node (VN) corresponds to M coded bits in c
- Fractional assignment of protograph VNs to modulation bits via matrix $\mathbf{A} = [a_{i,j}] \in \mathbb{R}^{m \times L}$, where $a_{i,j} =$ fraction of coded bits corresponding to VN j to be allocated to modulation bit i
- Baseline (sequential or random) bit mapper with $a_{i,j} = 1/m$, $\forall i, j$



- Bit mapper determines allocation of n coded bits to m modulation bits (N = n/m)
- Each protograph variable node (VN) corresponds to M coded bits in c
- Fractional assignment of protograph VNs to modulation bits via matrix $\mathbf{A} = [a_{i,j}] \in \mathbb{R}^{m \times L}$, where $a_{i,j} =$ fraction of coded bits corresponding to VN j to be allocated to modulation bit i
- Baseline (sequential or random) bit mapper with $a_{i,j} = 1/m$, $\forall i, j$
- A is optimized based on modified protograph extrinsic transfer function (P-EXIT) analysis (predicts BER for $M \to \infty$)

System Model	SC-LDPC Codes	Bit Mapper Optimization	Results	Conclusions	CHALMERS
O	000	O	●00	O	
					· · · · · · · · · · · · · · · · · · ·



• SC-LDPC code parameters: spatial length = 30, lifting factor = 3000, rate terminated = 0.741, rate tailbiting = 0.75



- SC-LDPC code parameters: spatial length = 30, lifting factor = 3000, rate terminated = 0.741, rate tailbiting = 0.75
- Windowed decoder: windows size = 5, iterations per window = 10



- SC-LDPC code parameters: spatial length = 30, lifting factor = 3000, rate terminated = 0.741, rate tailbiting = 0.75
- Windowed decoder: windows size = 5, iterations per window = 10
- Allocation of the coded bits of the tailbiting code to the least protected modulation bits of PM-64-QAM (green = 100%, red = 0%)





- SC-LDPC code parameters: spatial length = 30, lifting factor = 3000, rate terminated = 0.741, rate tailbiting = 0.75
- Windowed decoder: windows size = 5, iterations per window = 10
- Allocation of the coded bits of the tailbiting code to the least protected modulation bits of PM-64-QAM (green = 100%, red = 0%)



 Locally improved decoding convergence in the first spatial position leads to wave-like decoding behavior, similar to terminated SC-LDPC codes





spatial position
Comparison of Terminated and Tailbiting SC-LDPC Codes | Häger, Graell i Amat, Alvarado, Brännström, Agrell 9/11





predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)



Comparison of Terminated and Tailbiting SC-LDPC Codes | Häger, Graell i Amat, Alvarado, Brännström, Agrell 9/11





predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)



















spatial position

predicted BER per spatial position (optimized)







spatial position

predicted BER per spatial position (optimized)











































predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)







predicted BER per spatial position (optimized)




Decoding Behavior For The Same SNR



predicted BER per spatial position (optimized)



9/11



Decoding Behavior For The Same SNR



predicted BER per spatial position (optimized)



9/11



Decoding Behavior For The Same SNR



predicted BER per spatial position (optimized)



9/11

System Model	SC-LDPC Codes	Bit Mapper Optimization	Results	Conclusions	CHALMERS
O	000	O	00●	O	

Simulation Results



Simulation Results







• Gain of ≈ 0.55 dB at a BER of 10^{-5} for AWGN channel



• Gain of ≈ 0.55 dB at a BER of 10^{-5} for AWGN channel

15.6

 10^{-4}

 10^{-5}

14.4

14.8

SNR [dB]

15.2

Approximately the same gap to the BICM capacity for both optimized systems

16.0



- Gain of $\approx 0.55~\mathrm{dB}$ at a BER of 10^{-5} for AWGN channel
- Approximately the same gap to the BICM capacity for both optimized systems
- Nonlinear propagation at 40 Gbaud with 70 km spans (split-step Fourier simulation)



- Gain of $\approx 0.55~\mathrm{dB}$ at a BER of 10^{-5} for AWGN channel
- Approximately the same gap to the BICM capacity for both optimized systems
- Nonlinear propagation at 40 Gbaud with 70 km spans (split-step Fourier simulation)
- 0.55 dB gain translates into ≈ 3 span increase (13%)



- Gain of $\approx 0.55~\mathrm{dB}$ at a BER of 10^{-5} for AWGN channel
- Approximately the same gap to the BICM capacity for both optimized systems
- Nonlinear propagation at $40~{\rm Gbaud}$ with $70~{\rm km}$ spans (split-step Fourier simulation)
- 0.55 dB gain translates into ≈ 3 span increase (13%)
- Terminated code enables longer reach, at the expense of 1.2% decrease in spectral efficiency

Comparison of Terminated and Tailbiting SC-LDPC Codes | Häger, Graell i Amat, Alvarado, Brännström, Agrell 10 / 11

	Bit Mapper Optimization	Conclusions	
		•	CHALMERS
	C		

Conclusions



1. Unequal error protection of a nonbinary modulation format can be used to significantly improve performance of tailbiting SC-LDPC codes



- 1. Unequal error protection of a nonbinary modulation format can be used to significantly improve performance of tailbiting SC-LDPC codes
- With optimized bit allocation, terminated and tailbiting codes are competitive, i.e., spectral efficiency can be traded for transmission reach, at similar gap to capacity.



- 1. Unequal error protection of a nonbinary modulation format can be used to significantly improve performance of tailbiting SC-LDPC codes
- With optimized bit allocation, terminated and tailbiting codes are competitive, i.e., spectral efficiency can be traded for transmission reach, at similar gap to capacity.

Thank you!

FIBER-OPTIC COMMUNICATIONS RESEARCH CENTER